



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Teruo TAKIZAWA et al.

Application No.: 09/763,365

Filed: February 23, 2001

Group Art Unit: 2813

Examiner:

Thanh T. Nguyen

Docket No.: 108680

SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

RESPONSE TO RESTRICTION REQUIREMENT

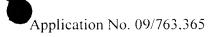
Director of the U.S. Patent and Trademark Office Washington, D.C. 20231

Sir:

For:

In reply to the Restriction Requirement mailed December 4, 2001, Applicants provisionally elect Group I, claims 1-7 and 14, drawn to a semiconductor device, classified in class 257, subclass 49, with traverse.

It is also respectfully submitted that the subject matter of all claims 1-14 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, it is respectfully submitted that the search and examination of the entire application could be made without serious burden. See MPEP §803 in which it is stated that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits even though it includes claims to distinct or independent inventions" (emphasis added). It is respectfully submitted that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.





Thus, withdrawal of the Restriction Requirement is respectfully requested.

Respectfully submitted,

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Date: January 3, 2002

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